MODEL NO : P0840XGF1MB00

SPEC VERSION : 1.2

ISSUED DATE: 2020-11-11

■**Preliminary Specification**
□**Final Product Specification**

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Customer :_____

Approved by	Notes

TIANMA Confirmed :

Prepared by	Checked by	Approved by

This technical specification is subjected to change without notice

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Record of Revision

Rev	Issued Date	Description	Editor
1.0	2020-09-08	Preliminary Specification release.	Bin Wang
1.1	2020-10-23	Update Mechanical Drawing in page 17,add OTP interface	Bin Wang
1.2	2020-11-11	Update the item name in the Mechanical Drawing in page 17.	Bin Wang

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1 General Specifications

Feature		Spec
Display Spec.	Size	8.4inch
	Resolution	1024x768
	Technology Type	a-Si
	Pixel Configuration	R.G.B. Vertical Stripe
	Pixel pitch(mm)	0.1665 x 0.1665
	Display Mode	SFT
	Surface Treatment	AG
	Viewing Direction	All Direction
Mechanical Characteristics	LCM (W x H x D) (mm)	199.5×149×9.7
	Active Area(mm)	170.496*127.872
	With /Without TSP	Without
	Matching Connection	CN1 : FI-SEB20P-HFE CN2 : FI-S6P-HFE(JAE) CN3：MSB24038P6
	LED Numbers	21 LEDs
	Weight (g)	TBD
Electrical Characteristics	Interface	LVDS
	Color Depth	16.7M
	Driver IC	NT51625TTH*2+NT52602TTH*1

Note 1: Requirements on Environmental Protection: Q/S0002.

Note 2: LCM weight tolerance: ± 5%

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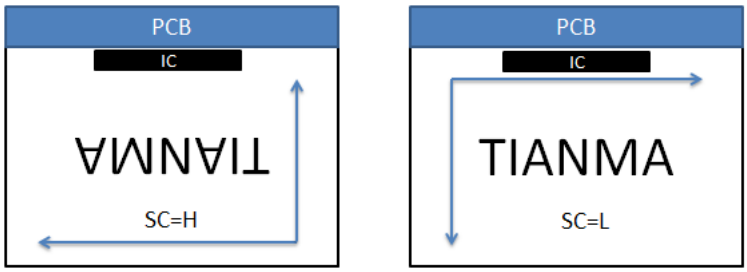
2 Input/Output Terminals

2.1 LCD Interface PINs

Matching Connector: FI-SEB20P-HFE

Pin No.	Symbol	I/O	Function	Remark
1	VCC	P	3.3V power supply	-
2	VCC	P	3.3V power supply	-
3	GND	P	Power Ground	-
4	GND	P	Power Ground	-
5	Link0-	I	Negative LVDS differential data input	Note2
6	Link0+	I	Positive LVDS differential data input	Note2
7	GND	P	Power Ground	
8	Link1-	I	Negative LVDS differential data input	Note2
9	Link1+	I	Positive LVDS differential data input	Note2
10	GND	P	Power Ground	
11	Link2-	I	Negative LVDS differential data input	Note2
12	Link2+	I	Positive LVDS differential data input	Note2
13	GND	P	Power Ground	
14	CLKIN-	I	Negative LVDS differential data input	Note2
15	CLKIN+	I	Positive LVDS differential data input	Note2
16	GND	P	Power Ground	
17	Link3	I	Negative LVDS differential data input	Note2
18	Link3+	I	Positive LVDS differential data input	Note2
19	MODE	I	H：8bit； L：6bit	-
20	SC	I	Reverse Scan control Low : Normal scan High or Open : Reverse scan	Note3-

Note1：I---Input, O---Output, P--- Power/Ground
Note2: All of the GND Pins should be connected to the system ground.
Note3: The function of the SC.



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2.2 CN2 pin assignment (Backlight interface)

Matching Connector type: FI-S6P-HFE(JAE)

Pin	Symbol	I/O	Description	Remark
1	VL	P	Power Supply Input Voltage	-
2	VL	P	Power Supply Input Voltage	-
3	GND	P	GND	-
4	GND	P	GND	-
5	BLEN	I	Backlight ON-OFF (High：ON, Low：OFF)	-
6	V_{PDIM}	I	Light Dimming Control (PWM)Input Voltage	-

3 Absolute Maximum Ratings

GND=0V

Item	Symbol	MIN	MAX	Unit	Remark
Power Voltage	VCC	-0.3	5	V	
Input voltage	VIN	-0.3	5	V	Note1
Backlight Power Voltage	VL	-0.3	26.5	V	
Backlight Input voltage	VBLIN	-0.3	26.5	V	Note2
Operating Temperature	Top	-30	80	℃	-
Storage Temperature	Tst	-40	90	℃	-
Relative Humidity Note2	RH	--	≤95	%	Ta≤40℃
		--	≤85	%	40℃＜Ta≤50℃
		--	≤55	%	50℃＜Ta≤60℃
		--	≤36	%	60℃＜Ta≤70℃
		--	≤24	%	70℃＜Ta≤80℃
Absolute Humidity	AH	--	≤70	g/m^3	Ta＞70℃

Table 3 Absolute Maximum Ratings

Note1: Input voltage include MODE,SC
Note2: Backlight Input voltage include BLEN, V PDIM
Note2: Ta means the ambient temperature.
It is necessary to limit the relative humidity to the specified temperature range.
Condensation on the module is not allowed.

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4 Electrical Characteristics

4.1 Driving TFT LCD Panel

VCC=3.3V，GND=0V，Ta = 25℃

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Logic supply voltage	VCC	3.2	3.3	3.4	V	Note1
System Ground	GND	-	0	-	V	
Input High Voltage	VIH	0.7xVCC		VCC	V	Note2
Input Low Voltage	VIL	GND		0.3xVCC	V	
LVDS differential input high threshold voltage	RxVTH	-	-	+200	mV	Note3
LVDS differential input low threshold voltage	RxVTL	-200	-	-		
Differential input voltage	|VID|	200	-	600	mV	
Differential input common mode voltage	RxVCM	1.0	1.2	1.7-|VID| /2	V	
Current of VCC Power supply	IVCC	-	TBD （360）	-	mA	Note4
Power consumption of VCC	PVCC	-	TBD （1188）	-	mW	Note5
Inrush current of VCC	Irush	-	TBD	-	A	

Table 4.1 LCD electrical characteristics

Note1: Indicate the subsequent version may be updated
Note2: Including MODE,SC……..
Note3: Refers to the LVDS waveform as shown below:
Note4: Test pattern: TBD（white）
Note5: VCC rising time >0.5ms.

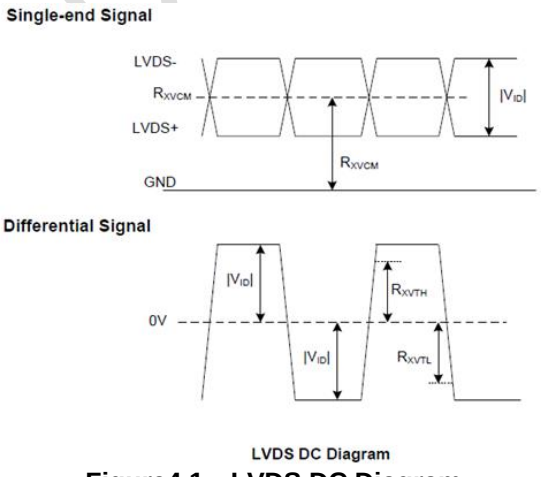


Figure4.1 LVDS DC Diagram

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4.2 Backlight Unit Driving Condition

LED GND=GND=0V, Ta = 25℃

Parameter		Symbol	min.	typ.	max.	Unit	Remarks
Power supply voltage		VL	10.8	12.0	13.2	V	Note1,Note3
Power supply current		IVL	-	TBD		mA	Note2
Input voltage for V_{PDIM} （ PWM ） signal	High	VIH	1.3	-	VL	V	-
	Low	VIL	0	-	0.5		
Input voltage for BLEN signal	High	VIH	1.3	-	VL		
	Low	VIL	0	-	0.5		
V_{PDIM}（PWM） frequency		f_{PWM}	100	-	1000	Hz	
V_{PDIM}（PWM） duty ratio		DR_{PWM}	1	-	100	%	
LED Life time		-	-	50000	--	Hrs	

Table 4.2 LED backlight characteristics

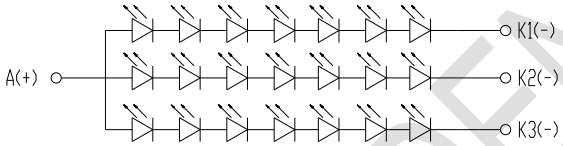


Figure4.2.1 LED connection of backlight

Note1: When designing of the power supply, take the measures for the prevention of surge voltage.
Note2: This value excludes peak current such as overshoot current. At the maximum luminance control.
Note3: The power supply lines (VL and GND) may have ripple voltage during luminance control of LED. There is the possibility that the ripple voltage produces acoustic noise and signal wave noise in audio circuit and so on.

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4.3 BLOCK DIAGRAM

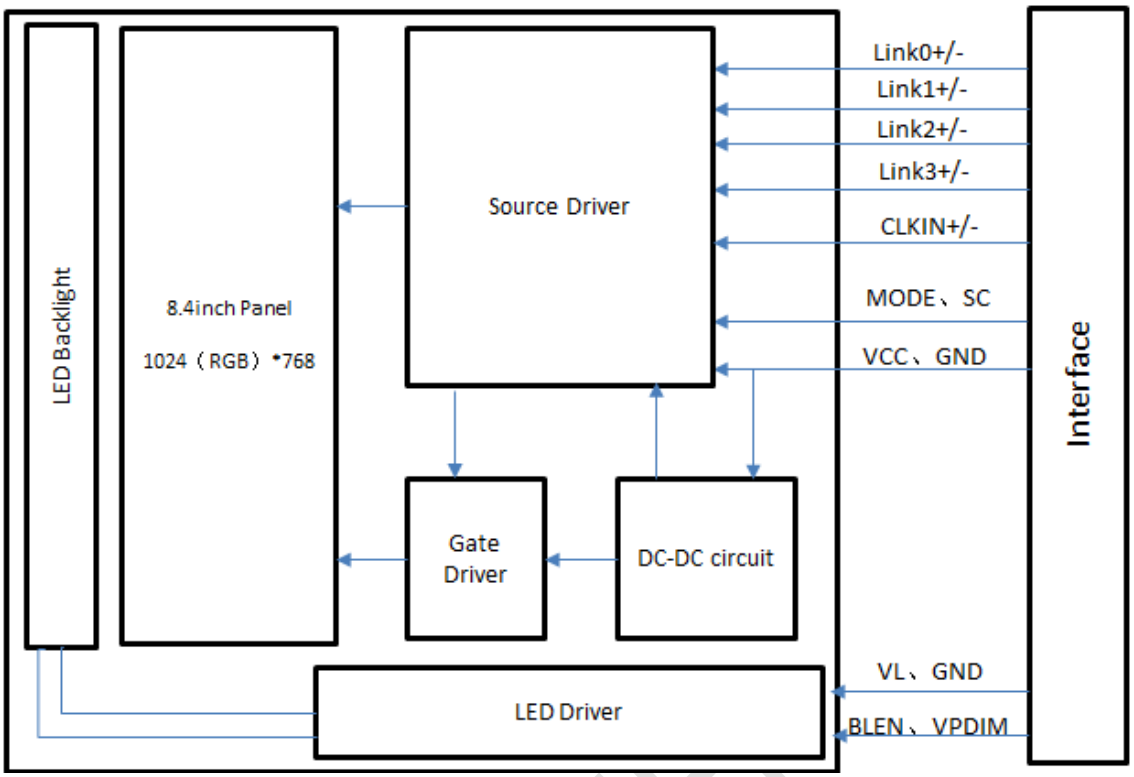
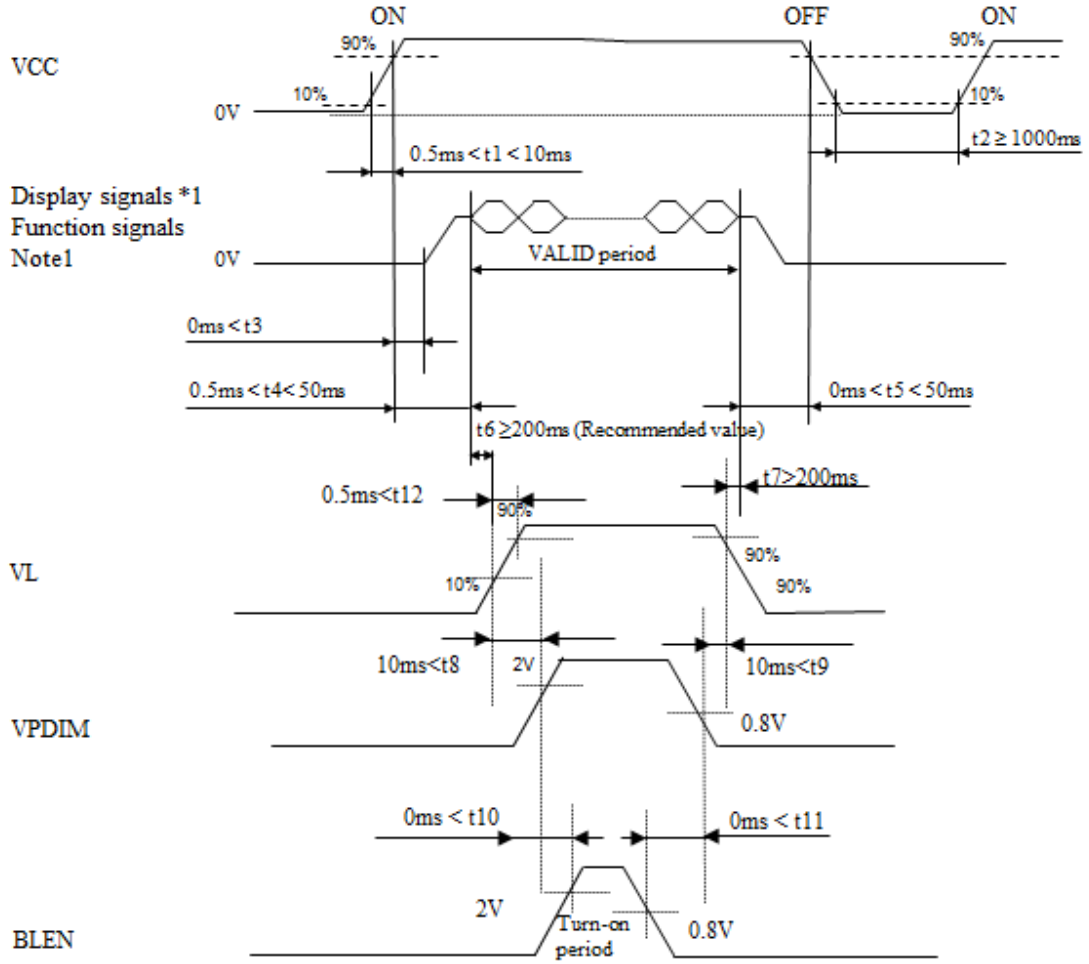


Figure 4.3.1 LCD Module Block Diagram

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4.4 LCD panel Power ON / OFF sequence



*1: Link0+/-, Link1+/-, Link 2+/-, Link 3+/-, CLKIN+/-

Note1: If some of display and function signals of this product are cut while this product is working, even if the signal input to it once again, it might not work normally. If a customer stops the display and function signals, VCC also must be shut down.

Note2: The backlight should be turned on within the valid period of display and function signals, in order to avoid unstable data display.

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5 Timing Chart

5.1 LVDS Interface Timing Characteristics

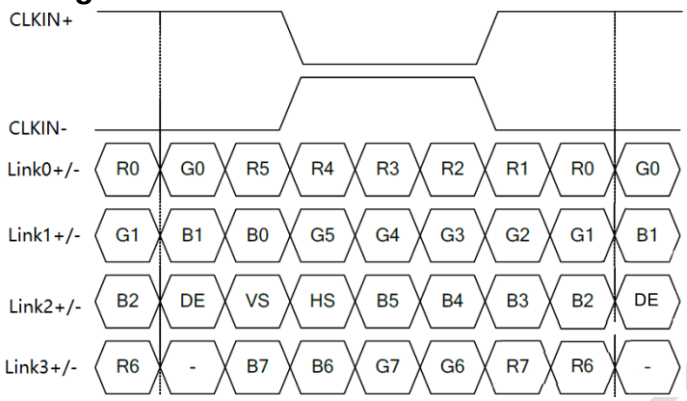


Figure 5.1.1 LVDS data map

Note：8-bit LVDS VESA

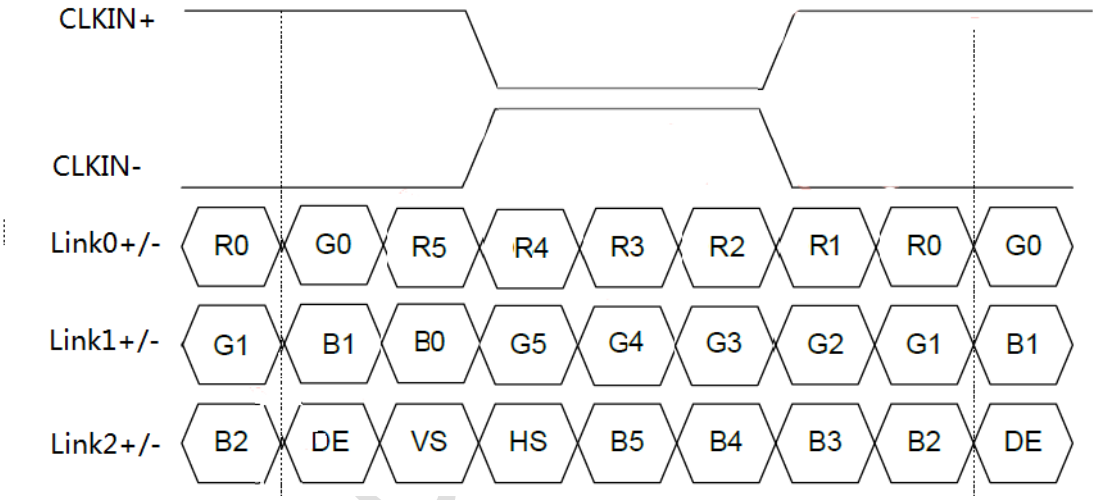


Figure5.1.2 LVDS data paraters

Note：6-bit LVDS VESA

Parameter	Symbol	Min.	Typ.	Max.	Unit
CLK frequency	FCLK	50.3	50.7	65.3	MHz
Horizontal display area	THD		1024		CLK
HS period time	TH	1084	1088	1214	CLK
HS blanking	THFP+THBP	60	64	190	CLK
Vertical display area	TVD		768		H
VS period time	TV	774	776	897	H
VS blanking	TVBP+TVFP	6	8	129	H

DE mode for 1024RGB*768

Figure5.1.2 LVDS data parameters

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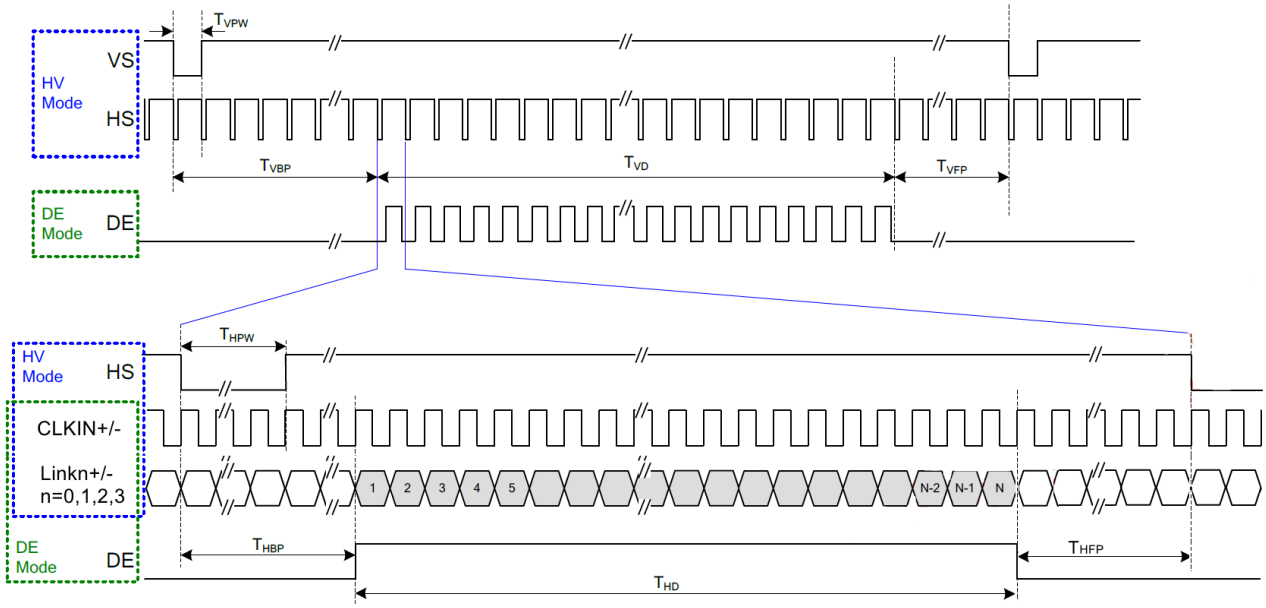


Figure5.1.3 Recommended input timing of LVDS transmitter

Note1: As shown in the figure above, the customer only needs to look at the DE mode section , instead of the SYNC section.

Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Clock frequency	FLVCLK	25	-	85	MHz	Refer to input timing table for each display resolution.
Clock Period	TLVCLK	11.76	-	40	nsec	
Clock high time	TLVCH	-	4/(7* RXFCLK)	-	ns	
Clock low time	TLVCL	-	3/(7* RXFCLK)	-	ns	
Input data skew margin	TRSKM	-	-	0.25	UI	VCC_IF=1.8V w/o SSC
Strobe width	TSW	0.5	-	-	UI	
1 data bit time	UI	-	1/7	-	TLV CLK	
Position 1	TPOS1	-0.25	0	0.25	UI	
Position 0	TPOS0	0.75	1	1.25	UI	
Position 6	TPOS6	1.75	2	2.25	UI	
Position 5	TPOS5	2.75	3	3.25	UI	
Position 4	TPOS4	3.75	4	4.25	UI	
Position 3	TPOS3	4.75	5	5.25	UI	
Position 2	TPOS2	5.75	6	6.25	UI	
PLL wake-up time	TenPLL	-	-	150	us	
Modulation Frequency	SSCMF	-	-	93	KHz	
Modulation Rate	SSCMR	-3	-	+3	%	LVDS clock = 81MHz, center spread

Figure5.1.4 LVDS Data Skew

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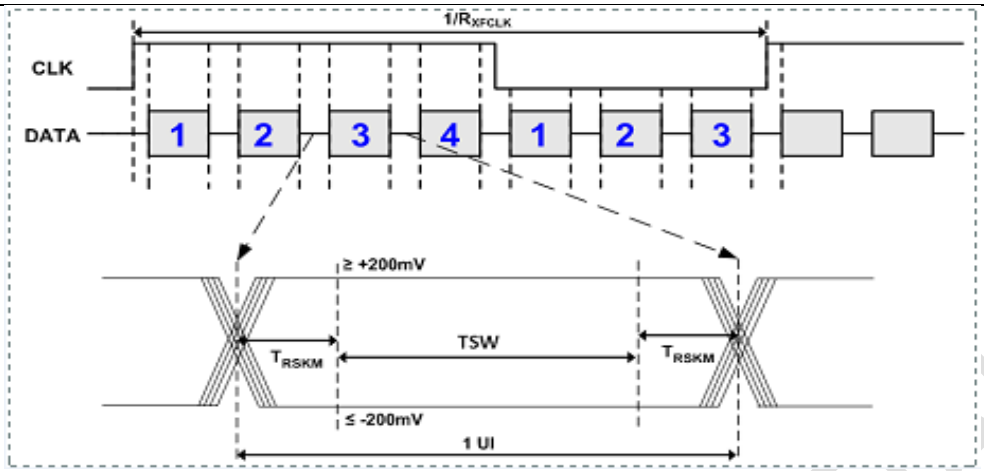


Figure5.1.5LVDS Data Skew

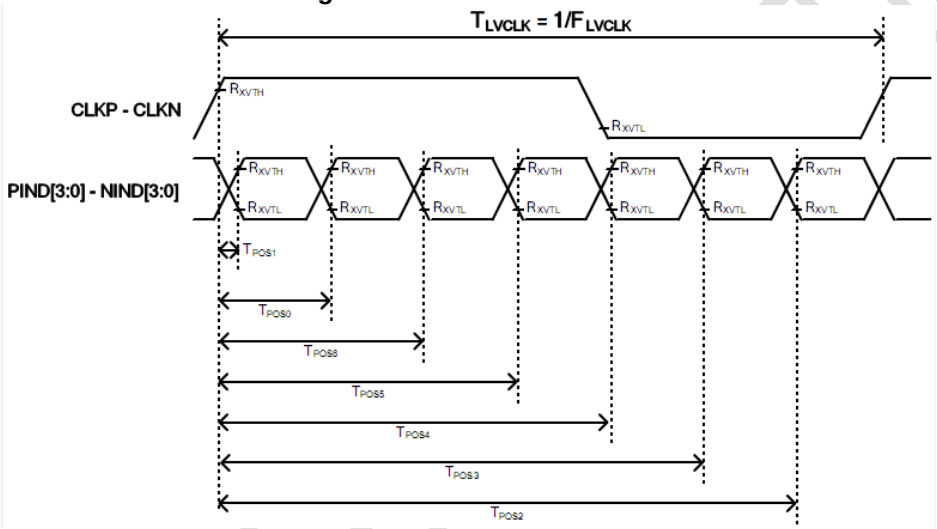


Figure 5.1.6 LVDS input timing

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6 Optical Characteristics

Item		Symbol	Condition	Min	Typ	Max	Unit	Remark
View Angles		θT	CR≧10	80	88	--	Degree	Note2,3
		θB		80	88	--		
		θL		80	88	--		
		θR		80	88	--		
Contrast Ratio		CR	θ=0°	800	1000	--		Note 3
Response Time		T_{ON}	25℃	--	25	30	ms	Note 4
		T_{OFF}						
Chromaticity	**White**	x	Backlight is on	--	TBD	--		Note 1,5
		y		--	TBD	--		
	Red	x		--	TBD	--		Note 1,5
		y		--	TBD	--		
	Green	x		--	TBD	--		Note 1,5
		y		--	TBD	--		
	Blue	x		--	TBD	---		Note 1,5
		y		--	TBD	--		
Uniformity		U					%	Note 6
NTSC				65	70	--	%	Note 5
Luminance		L			600		cd/m^2	Note 7

Test Conditions:

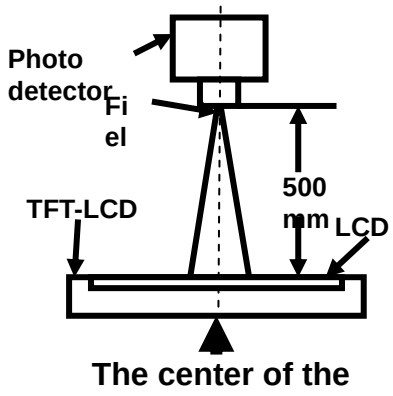
1. I_F= **XX** mA, and the ambient temperature is 25℃.
2. The test systems refer to Note 1 and Note 2.

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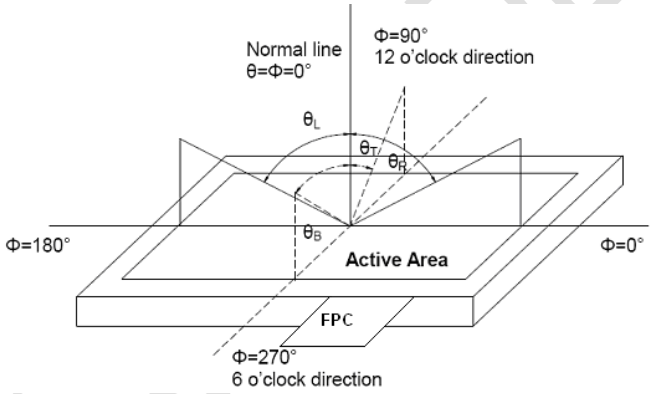
Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 Minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD .



Note 3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

“White state “: The state is that the LCD should drive by Vwhite.

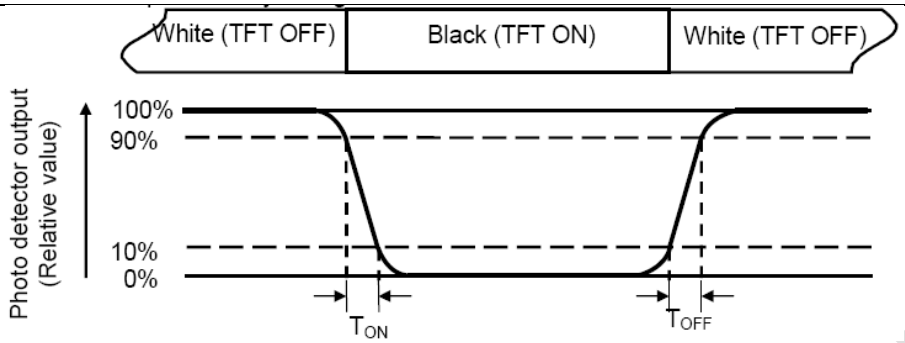
“Black state”: The state is that the LCD should drive by Vblack.

Vwhite: To be determined Vblack: To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

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Note 5: Definition of color chromaticity (CIE1931)

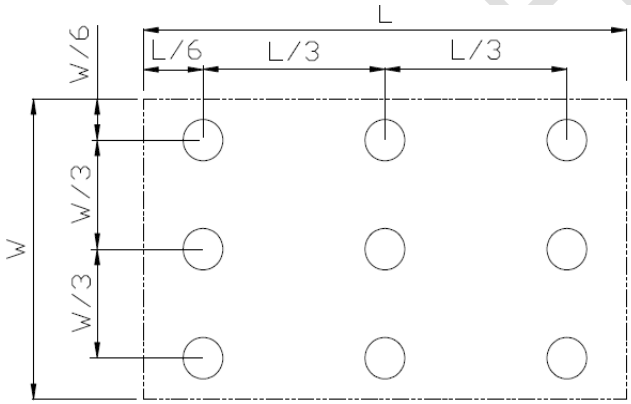
Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) = Lmin/ Lmax

L-------Active area length W----- Active area width



Lmax: The measured Maximum luminance of all measurement position.

Lmin: The measured Minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.

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7 Environmental / Reliability Test

No	Test Item	Condition	Remarks
1	High Temperature Operation	Ta = +80℃,500 hours	IEC60068-2-1 GB2423.2
2	Low Temperature Operation	Ta = -30℃, 500 hours	IEC60068-2-1 GB2423.1
3	High Temperature Storage	Ta = +90℃, 500 hours	IEC60068-2-1 GB2423.2
4	Low Temperature Storage	Ta = -40℃, 500 hours	IEC60068-2-1 GB2423.1
5	Storage at High Temperature and Humidity	Ta = +60℃, 90% RH max,500hours	IEC60068-2-78 GB/T2423.3
6	Thermal Shock (non-operation)	-30℃ 30 min~+80℃ 30 min, Change time:5min, 100 Cycle	Start with cold temperature, End with high temperature, IEC60068-2-14,GB2423.22
7	ESD	C=150pF,R=330Ω,5point/panel Air:±15Kv,5times; Contact:±8Kv,5times (Environment:15℃~35℃, 30%~60%.86Kpa~106Kpa)	IEC61000-4-2 GB/T17626.2
8	Vibration Test (Non Op)	5~100HZ，19.60m/s2 1min/cycle 120times Per X\Y\Z	IEC60068-2-6 GB/T17626.6
9	Mechanical Shock (Non Op)	539m/s2, 11ms 5times ±X、±Y、±Z	IEC60068-2-27 GB/T2423.5

Note1: Ts is the temperature of panel's surface.

Note2: Ta is the ambient temperature of sample.

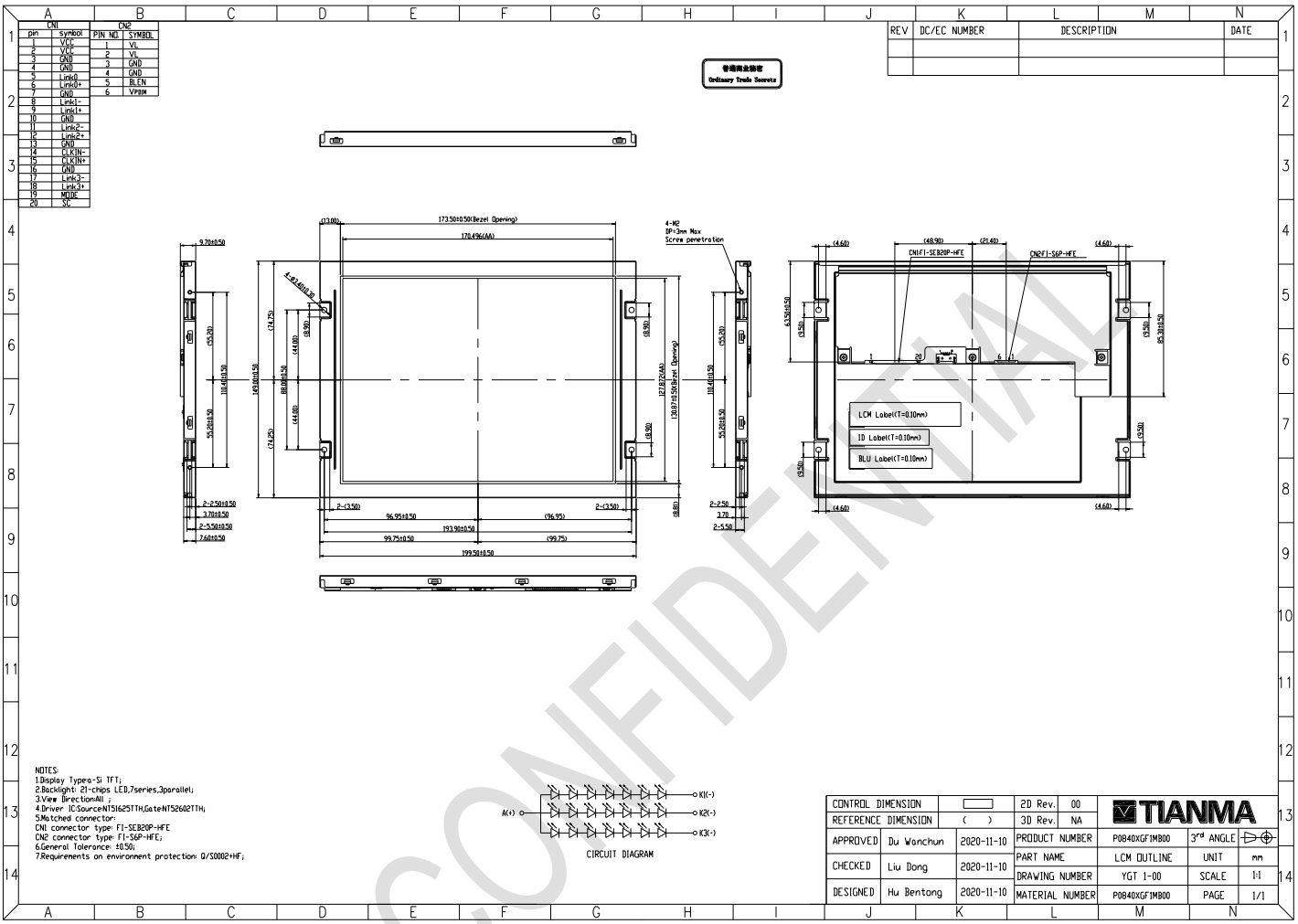
Note3: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

Note 4: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

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8 Mechanical Drawing



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9 Packing Drawing

(如果客户对标签或 Label 有特殊要求，请注明)

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10 Precautions for Use of LCD Modules

10.1 Handling Precautions

10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5 If the display surface is contaMinated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents

10.1.6 Do not attempt to disassemble the LCD Module.

10.1.7 If the logic circuit power is off, do not apply the input signals.

10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

10.1.8.1 Be sure to ground the body when handling the LCD Modules.

10.1.8.2 Tools required for assembly, such as soldering irons, must be properly ground.

10.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

10.1.8.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ～ 40℃ Relatively humidity: ≤80%

10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 Transportation Precautions

10.3.1 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

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