



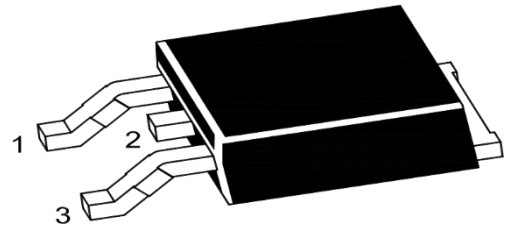
PJM100H02NTE

N-Channel Power MOSFET

Descriptions

- Fast Switching
- Low $R_{DS(ON)}$ and Gate Charge
- Low Reverse Transfer Capacitance
- 100% Single Pluse Avanalanche Energy Test

TO-252



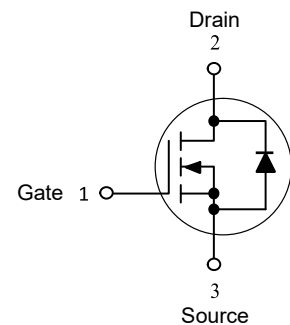
Features

- $V_{DS} = 1000V$, $I_D = 2A$
- $R_{DS(ON)} < 8.8 \Omega$ (@ $V_{GS} = 10V$)
- ESD Protected $> 4kV$ (HBM)
- MSL: 1 Level

Applications

- Power Switch
- Adaptor, Charger

Schematic Diagram



Absolute Maximum Ratings

Ratings at $T_C = 25^\circ C$ unless otherwise specified.

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DSS}	1000	V
Gate-Source Voltage	V_{GSS}	± 30	V
Continuous Drain Current	I_D	$T_C = 25^\circ C$	2
		$T_C = 100^\circ C$	1.2
Pulsed Drain Current ¹	I_{DM}	8	A
Avalanche Current ²	I_{AR}	3.3	A
Single Pulse Avalanche Energy ²	E_{AS}	80	mJ
Avalanche Energy ,Repetitive ¹	E_{AR}	15	mJ
Power Dissipation	P_D	95	W
Junction and Storage Temperature Range	T_J, T_{STG}	150, -55 to 150	$^\circ C$
Thermal Characteristics			
Parameter	Symbol	Typ.	Units
Maximum Junction-to-Ambient	$R_{\theta JA}$	100	$^\circ C/W$
Maximum Junction-to-Case	$R_{\theta JC}$	3.13	$^\circ C/W$



Electrical Characteristics (T_c = 25°C)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Static Parameters						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250μA, V _{GS} =0V	1000	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =1000V, V _{GS} =0V T _A =25°C	--	--	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±30V	--	--	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2	3	4	V
Static Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =1.0A	--	7.8	8.8	Ω
Dynamic Parameters						
Forward Transconductance	g _{FS}	V _{DS} =15V, I _D =2.0A	--	2.1	--	S
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =25V, f=1MHz	--	380	--	pF
Output Capacitance	C _{oss}		--	40	--	pF
Reverse Transfer Capacitance	C _{rss}		--	4	--	pF
Switching Parameters						
Total Gate Charge	Q _g	V _{GS} =10V, V _{DD} =500V, I _D =2A	--	15	--	nC
Gate Source Charge	Q _{gs}		--	2.1	--	nC
Gate Drain Charge	Q _{gd}		--	6	--	nC
Turn-On DelayTime	t _{D(on)}	V _{GS} =10V, V _{DD} =500V, I _D =2A R _g =12Ω	--	8	--	ns
Turn-On Rise Time	t _r		--	6	--	ns
Turn-Off DelayTime	t _{D(off)}		--	36	--	ns
Turn-Off Fall Time	t _f		--	15	--	ns
Body Diode Reverse Recovery Time	t _{rr}	I _F =2A, V _{GS} =0, T _J =25°C di/dt=100A/μs	--	500	--	ns
Body Diode Reverse Recovery Charge	Q _{rr}		--	1.2	--	uC
Body Diode Forward Voltage	V _{SD}	I _F =2A, V _{GS} =0V	--	1.5	--	V
Body Diode Continuous Current	I _{SD}		--	--	2	A
Body Diode Maximum Pulse Current	I _{SM}		--	--	8	A

Notes: 1. Repetitive rating; Pulse width limited by maximum junction temperature;

2. L=10.0mH, I_D=2.0A, start T_J=25°C



Electrical Characteristics Curves

Fig. 1. Output Characteristics @ 25°C

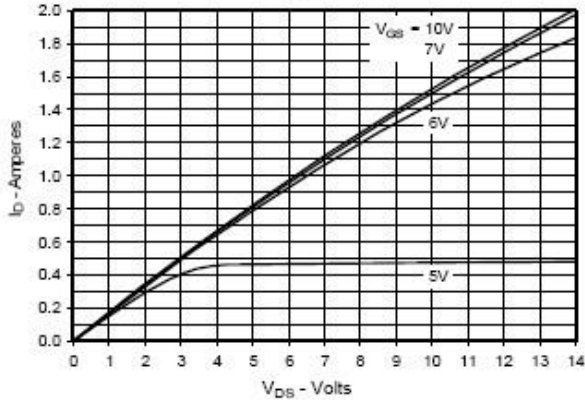


Fig. 2. Extended Output Characteristics @ 25°C

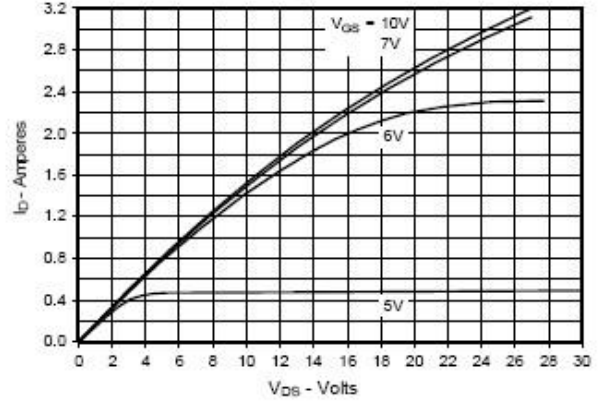


Fig. 3. Output Characteristics @ 125°C

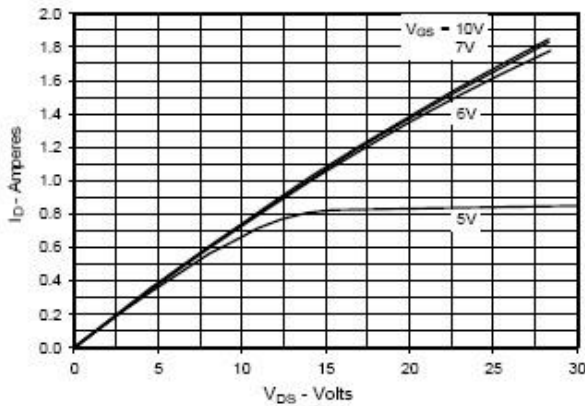


Fig. 4. $R_{DS(on)}$ Normalized to $I_D = 1A$ Value vs. Junction Temperature

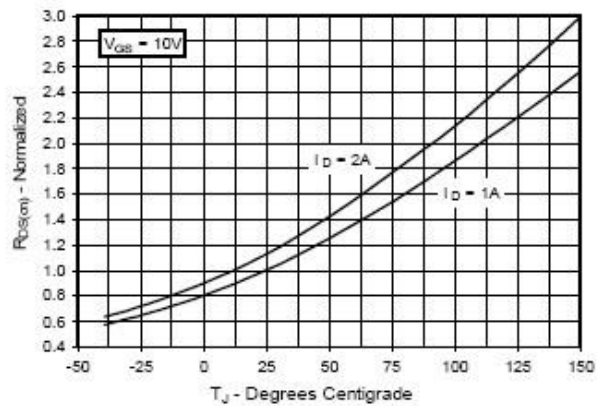


Fig. 5. $R_{DS(on)}$ Normalized to $I_D = 1A$ Value vs. Drain Current

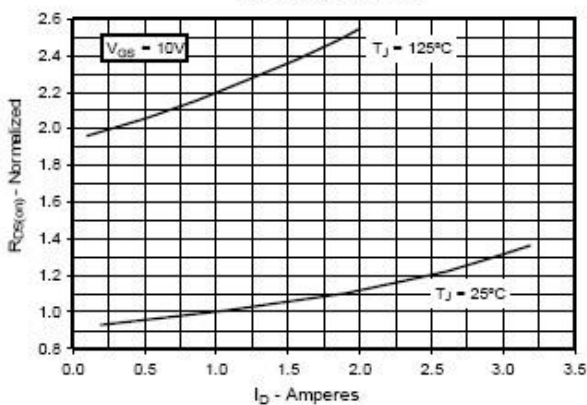
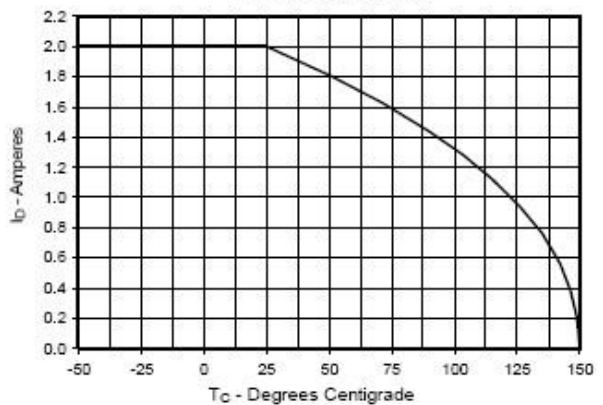


Fig. 6. Maximum Drain Current vs. Case Temperature





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Fig. 7. Input Admittance

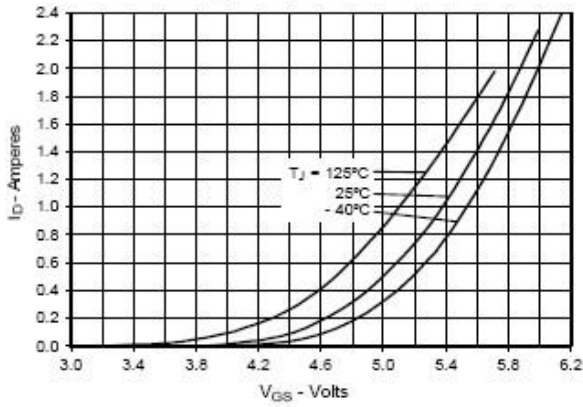


Fig. 8. Transconductance

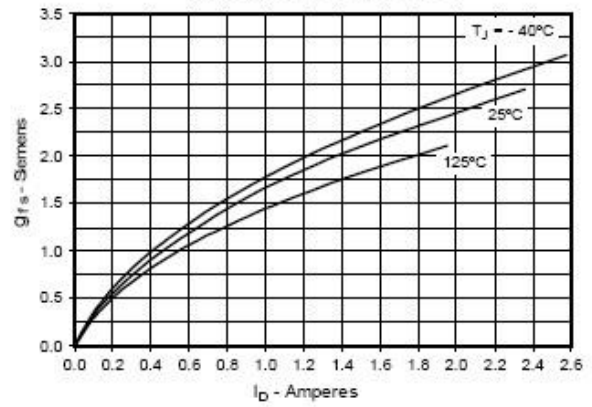


Fig. 9. Forward Voltage Drop of Intrinsic Diode

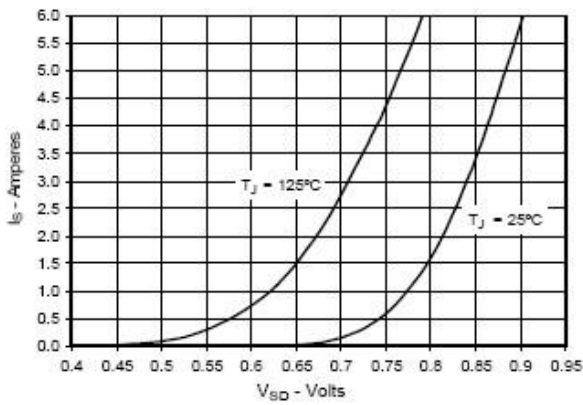


Fig. 10. Gate Charge

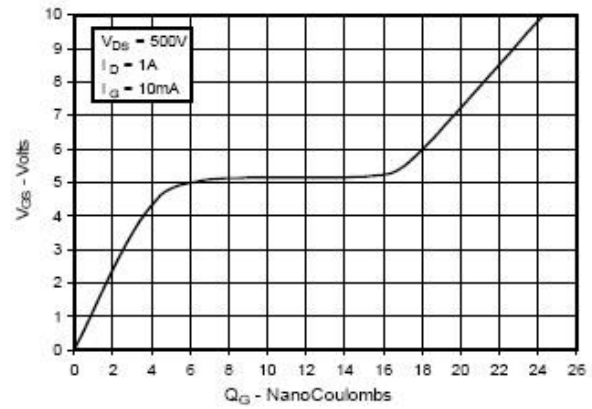


Fig. 11. Capacitance

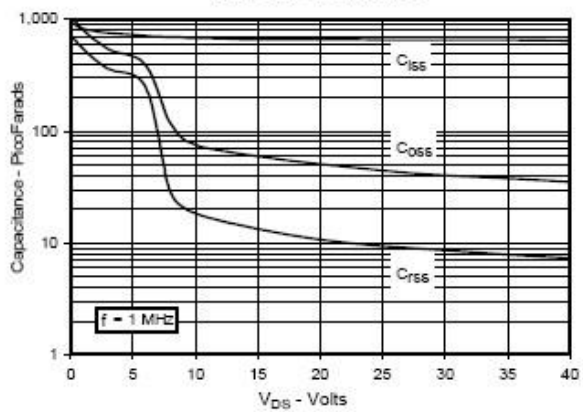
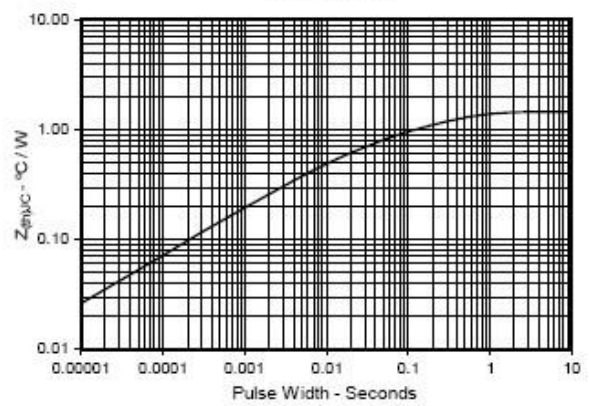


Fig. 12. Maximum Transient Thermal Impedance



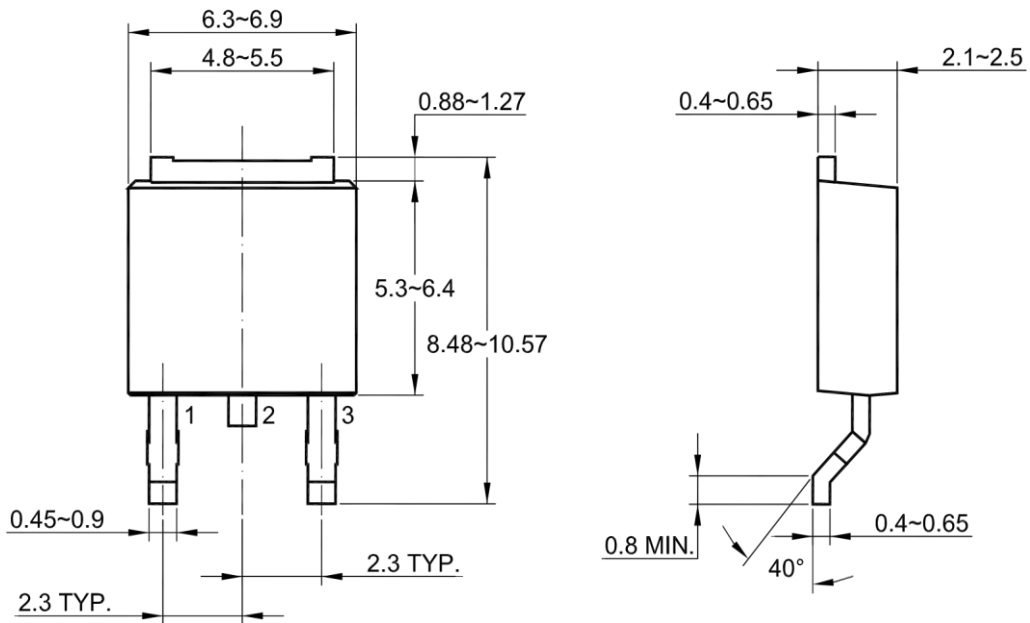


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Package Outline (TO-252)

Unit: mm



Recommended Soldering Footprint

Unit: mm

